

## CLAIMS

1 1. A scalable processing system, comprising:

2 a memory device that includes a plurality of executable program instructions, wherein  
3 each of said executable program instructions includes a timetag data field indicative of the  
4 nominal sequential order of said associated executable program instructions; and

5 a plurality of processing elements, which are configured and arranged to receive  
6 executable program instructions from said memory device, wherein each of said processing  
7 elements executes executable instructions having the highest priority as indicated by the state of  
8 said timetag data field.

Patent 00555555